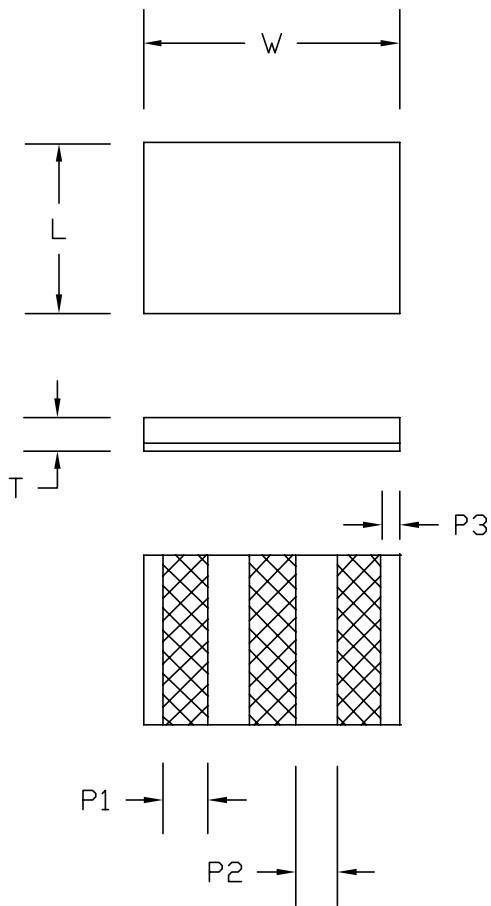
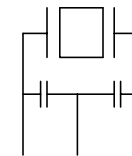
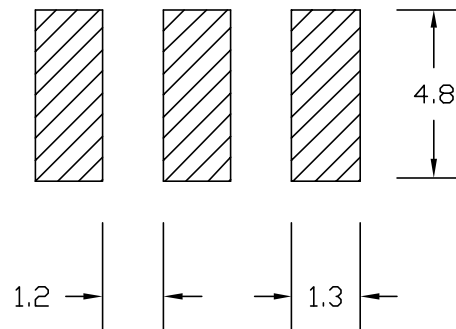


REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED



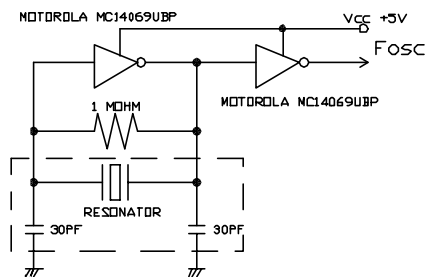
Recommend PCB Layout

SCHEMATIC

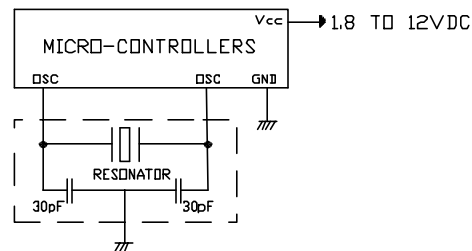


NOTES:

1. RESENANT FREQUENCY: 1.8432 MHz
2. FREQUENCY TOLERANCE: +/- 0.5% at 25C
3. TEMPERATURE RANGE: -20C to +80C
4. AGING: +/- 0.5% Max. IN 10 YEARS, ROOM TEMP.
5. BUILD-IN LOAD CAPACITORS: TWO 30pF
6. ABSOLUTE IMPEDANCE AT RESONANT: 30 OHMS MAX.
7. DIMENSION in mm: L=3.4, W=7.4, T=1.8
P1=1.2, P2=1.3, P3=0.6
8. MARKING: "1.84M", "ZTSC1.84M", "ZTTC1.84M"



CMOS STANDARD CIRCUIT



TYPICAL APPLICATION

UNLESS OTHERWISE SPECIFIED: DIMENSIONS ARE IN INCHES TOLERANCES	
DECIMALS XX ± .05	ANGULAR XXX ± .010
DO NOT SCALE DRAWING	
DRAWN J. YOUNG	DATE 12-02
CHECKED	
DESIGN	

INTEGRITY TECHNOLOGY			
CERAMIC RESONATOR			
SIZE A	FSCM NO.	DWG NO. ZTSC1.84M	REV A
SCALE NONE	ZTSC-1-84M	SHEET 1 OF 1	